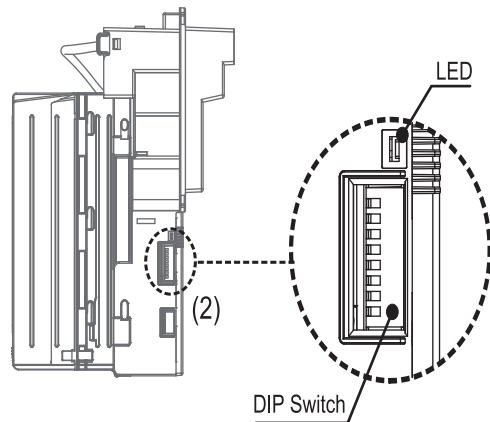


XBA-AMD2 Option Switch Settings

Supported bill AMD 1000, 2000 2bills.

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
★ Reject AMD 1000	ON							
★ Accept AMD 1000	OFF							
★ Reject AMD 2000		ON						
★ Accept AMD 2000		OFF						
★ Reserved			ON					
★ Reserved			OFF					
★ Reserved				ON				
★ Reserved				OFF				
★ Reserved					ON			
★ Reserved					OFF			
★ Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
★ Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
★ Stack Banknote when Power-up							ON	
★ Reject Banknote when Power-up							OFF	
★ Reserved								ON
★ Reserved								OFF

This dip switch is located at the side of XBA.



Currency Assign Data

Interface	Pulse	RS232 A0	ICT104U	ccNet	MDB
Bill value					
BV1	AMD 1000	AMD 1000	AMD 1000	AMD 1000	AMD 1000
BV2	AMD 2000	AMD 2000	AMD 2000	AMD 2000	AMD 2000

RS232 A0 INFORMATION

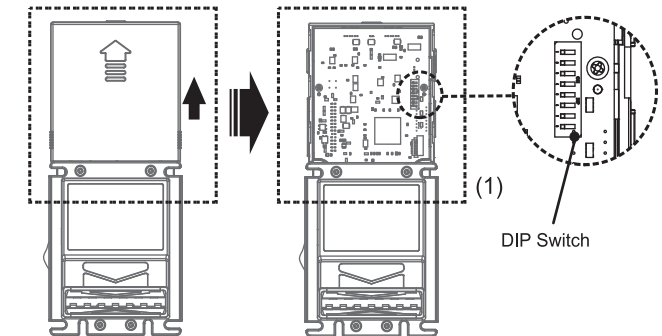
Bill Type Data	
Code	Denomination
1	Reserved
2	Reserved
3	AMD 1000
4	AMD 2000
5	Reserved
6	Reserved
7	Reserved
8	Reserved
9	Reserved

XBA-AMD2 (Pulse/MDB/RS232 A0/ccNet/ICT104U)

FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8	
Inhibit Active HIGH	ON								
★ Inhibit Active LOW	OFF								
★ Reserved		ON							
★ Reserved		OFF							
★ Reserved			ON						
★ Reserved			OFF						
★ Reserved				ON					
★ Reserved				OFF					
Pulse Mode	MDB Mode		ccNet Mode						
★ Credit-Pulse Normal HIGH	Scaling Factor (SF) = 100 Decimal Point Position(DPP) = 2		9600 Baud Rate			ON			
Credit-Pulse Normal LOW	Scaling Factor (SF) = 1 Decimal Point Position(DPP) = 0		19200 Baud Rate			OFF			
Pulse Mode						ON			
★ Other Mode						OFF			
★ 1 pulse / AMD 1000	MDB Mode							OFF	OFF
2 pulses / AMD 1000	RS232 A0 Mode							OFF	ON
4 pulses / AMD 1000	ccNet Mode							ON	OFF
20 pulses / AMD 1000	ICT104U Mode							ON	ON

★ Manufacture setting

This dip switch is located on the CPU board, remove the CPU board cover first.



**After setting dip switch of the credit pulses,you should reset the bill acceptor again.

Note : 1.No sleep function.
2.Calibration card is needed.